



# PSN ENGINEERING COLLEGE

AN ISO 9001-2015 CERTIFIED INSTITUTION

(Approved by AICTE and Affiliated to Anna University)

Melathediyoor, Palayamkottai Taluk, Tirunelveli District, Pin - 627 152.

Web Site : [www.psnec.ac.in](http://www.psnec.ac.in), E-mail: [989psnec@gmail.com](mailto:989psnec@gmail.com)

☎ : 04634 - 279078, 279079 Fax : 04634 - 279078, 279681



Date 11.05.2022

To

Dr G RAJAKUMAR, B.E, M.B.A, M.E, Ph.D,  
Professor of Electronics and Communication Engineering,  
Francis Xavier Engineering College,  
Vannarpettai, Tirunelveli- 627003

Dear Sir,

Greetings from PSN Engineering College, Tirunelveli, Tamil Nadu

We take immense pleasure to invite you to act as a resource person and give a Technical seminar to our Electronics and Communication Engineering department students on 13<sup>th</sup> May 2022.

Kindly accept our invitation for the above program and Looking forward to your positive reply.

Thanking you.



PRINCIPAL

Dr. M.S. RAVIKUMAR  
Principal  
PSN ENGINEERING COLLEGE  
Melathediyoor, Palayamkottai  
Tirunelveli - 627 152

# PERMISSION LETTER FROM ECE

FROM

MR. P. BABU,  
HOD / Electronics and Communication Engineering .  
PSN Engineering College ,  
Tirunelveli - 627152 .

TO

The Principal,  
PSN Engineering College ,  
Tirunelveli - 627152 .

Subj : Asking Permission Regarding one Day Seminar  
Respected Sir, (on-line mode)

As for our Electronics and Communication Engineering, we have planned to organize the One Day Seminar (Technical) on 13-05-2022 in PSNEC Seminar Hall. kindly give us permission to conduct the one Day Technical Seminar on the above mentioned date.

Thanking You.

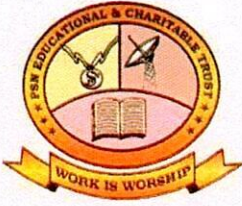
Date: 11.05.2022



Yours Sincerely

[P. BABU, HOD / ECE]





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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### Report on Webinar

<b>Name of the Program/Topic</b>	Webinar/Overview of VLSI Physical Design
<b>Conducted on</b>	13 / 05 / 2022 (Friday)
<b>Duration</b>	2 Hours (10 am to 12:00 pm)
<b>Mode</b>	Online
<b>Google Meet</b>	<a href="https://meet.google.com/ssy-bikm-oma">https://meet.google.com/ssy-bikm-oma</a>
<b>Number of Participants</b>	48
<b>Department/Year</b>	Electronics and Communication Engineering / II,III,&IV
<b>Resource Person Detail</b>	Dr.G.RAJAKUMAR, B.E, M.B.A, M.E, Ph.D, Professor of Electronics and Communication Engineering, Francis Xavier Engineering College, Vannarpettai, Tirunelveli- 627003
<b>Objective of the Webinar</b>	<ol style="list-style-type: none"><li>1. To provide students an understanding of overall VLSI Design concepts.</li><li>2. To provide students an understanding of the expectations of industry</li><li>3. To improve employability skills of students in the field of Design engineering</li><li>4. To bridge the skill gaps and make students industry ready</li><li>5. To make students better prepared to meet industry demands as well as develop their own interest</li></ol>



## REPORT:

The Department of Electronics and Communication Engineering has organized a webinar on **“Overview of VLSI Physical Design”** on **13<sup>th</sup> May, 2022** (Friday) between **10:00 AM to 12:00 PM**, through the **Google meet platform**. The lecture in this webinar was given by **Dr.G.RAJAKUMAR**, Professor of Electronics and Communication Engineering, Francis Xavier Engineering College, Tirunelveli.

Assistant Prof. S.Pitchaiah, Department of Electronics and Communication Engineering introduced the resource person. **Dr.G.RAJAKUMAR** delivered a lecture on the topic **“Overview of VLSI Physical Design”**. He explained about the introduction of the Basics of VLSI Design Concepts and its materials. He specifically focused on **physical layout of VLSI, maintenance, industrial design** views in VLSI platform and also highlighted the **opportunities** in this field. The queries raised by the students were clarified very well at the end of the presentation.

The resource person thanked the organizers for arranging the live webinar. At the end of the webinar Assistant Prof. S.Pitchaiah delivered the vote of thanks. He thanked the resource person, Management, Principal, Director, HoD, Faculty members, participants for their active support for making the programme to be successful.

## OUTCOMES:

At the end of this webinar, the students:

1. Gain the knowledge on basics of VLSI and embedded platform.
2. Gain the knowledge about the VLSI Design Platform.
3. Able to understand the VLSI Design layout in the embedded industry
4. Able to understand the importance of role of VLSI in the industrial sector
5. Able to understand the better opportunities in embedded industry





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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING PHOTO GALLERIES OF WEBINAR ON OVERVIEW OF VLSI PHYSICAL DESIGN

**PSN ENGINEERING COLLEGE**  
Melathediyoor, Tirunelveli -627 152  
(APPROVED BY AICTE, AFFILIATED TO ANNA UNIVERSITY )

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

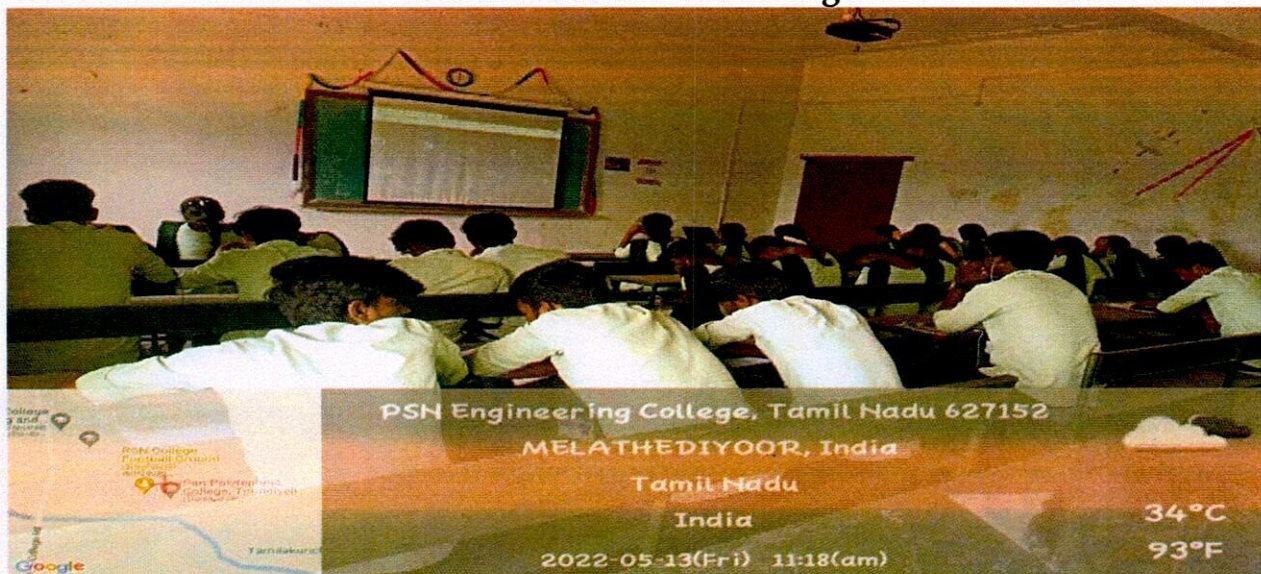
We invite you for a webinar on  
**“ Overview of VLSI Physical Design “**



**13 MAY 2022 (FRIDAY)**  
**10.00 AM TO 12.00 PM IST**  
**GOOGLE MEET LINK :**  
**<https://meet.google.com/ssy-bikm-oma>**

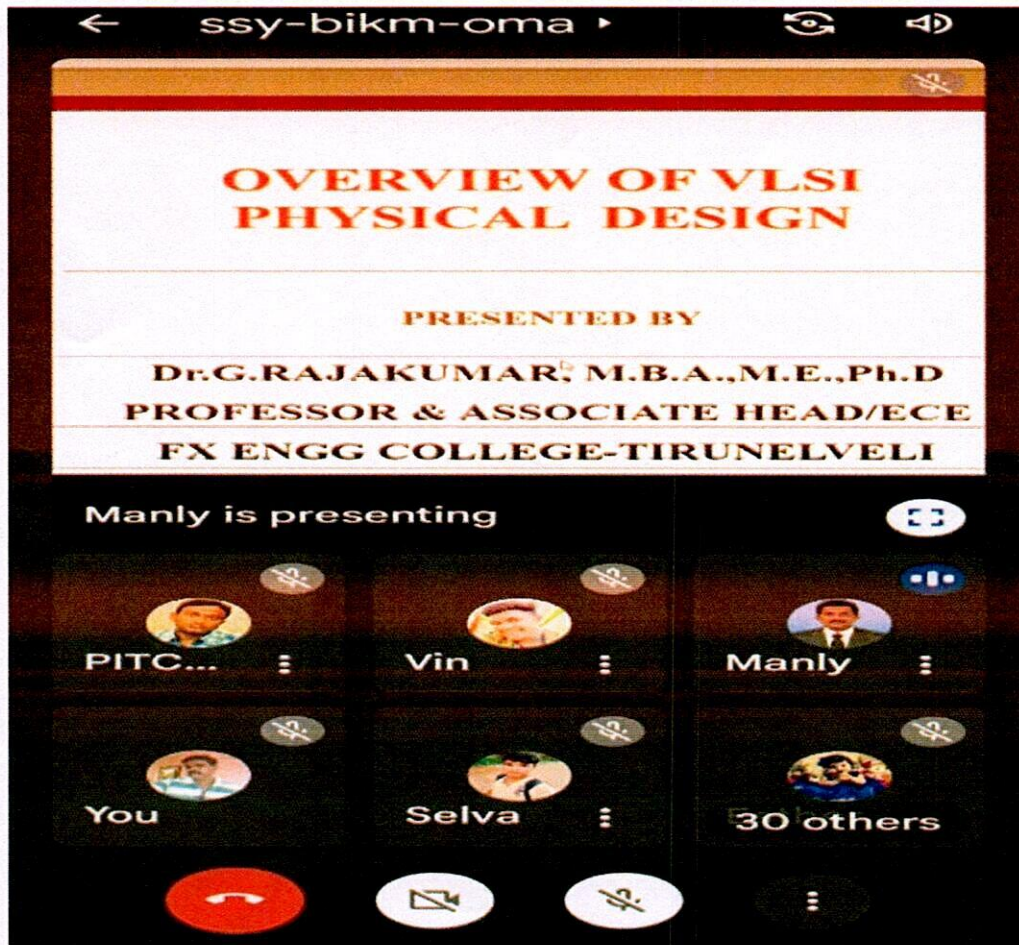
**SPEAKER**  
**Dr.G.RAJAKUMAR, B.E, M.B.A, M.E, Ph.D,**  
Professor of ECE.  
Francis Xavier Engineering College, Tirunelveli

Online Webinar Poster Image



Participants attending the Webinar



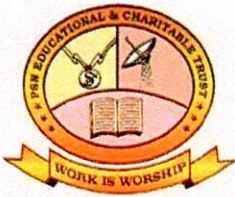


Invited speaker, Dr.G.RAJAKUMAR, Professor of ECE, Francis Xavier Engineering College delivering lecture on the topic 'OVERVIEW OF VLSI PHYSICAL DESIGN' with Power Point presentation



Participants attending the Webinar and asking their queries to the Honorable Speaker





# PSN ENGINEERING COLLEGE

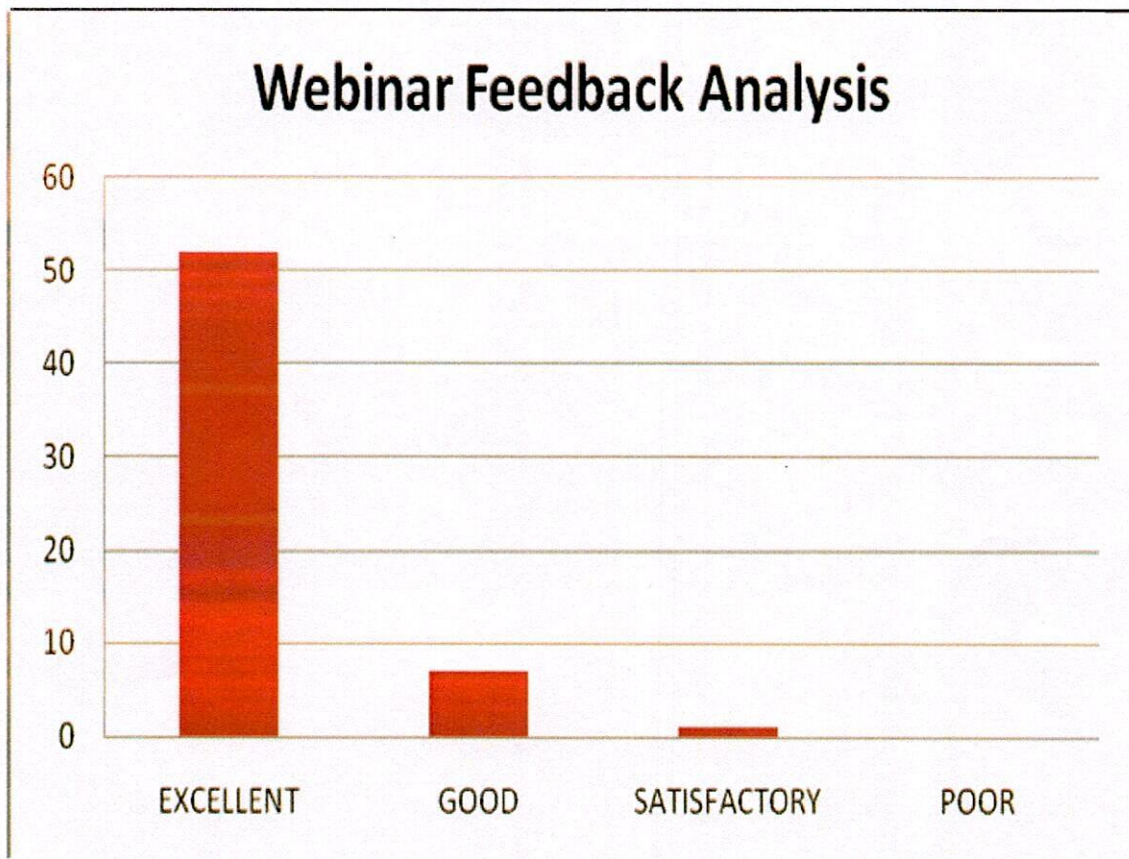
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### FEEDBACK ANALYSIS OF WEBINAR



*[Signature]*  
24/5/22  
Coordinator

*[Signature]*  
HOD  
24/5/22  
Head Of The Department  
Electronics & Commn. Department  
PSN ENGINEERING COLLEGE  
Melathediyoor - 627 152.

*[Signature]*  
Principal  
9/20  
Dr. M.S. RAVIKUMAR  
Principal  
PSN ENGINEERING COLLEGE  
Melathediyoor, Palayamkottai  
Tirunelveli - 627 152